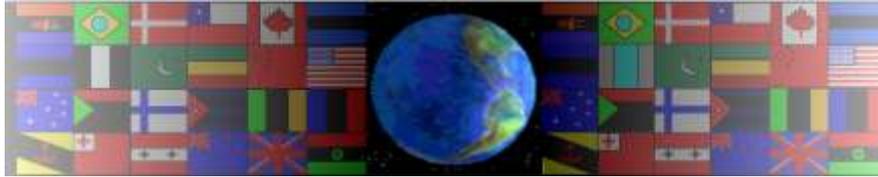


DIGITAL LOGIC CIRCUITS

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On behalf of The World Association of Technology Teachers

W.A.T.T.



World Association of Technology Teachers

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DIGITAL LOGIC TABLES AND GATES

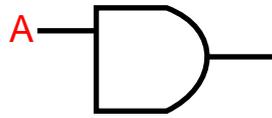
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1. Complete the AND and NAND logic tables and symbols seen below.

AND gate

A	B	Q
0		0
	1	
1	0	0
1		

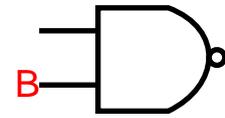
INPUT OUTPUT



NAND gate

A	B	Q
	0	1
0	1	
1		1
	1	

INPUT OUTPUT



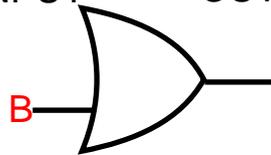
2. How does the NAND gate differ from an AND gate?

3. Complete the OR and NOR logic tables and symbols seen below.

OR gate

A	B	Q
0		0
	1	1
1		1
1	1	

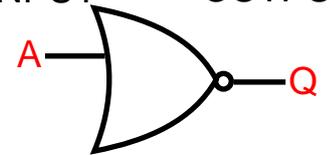
INPUT OUTPUT



NOR gate

A	Q
	0
0	1
1	
	1

INPUT OUTPUT



4. How does the NOR gate differ from an OR gate?

5. Complete the INVERTER table and symbols seen below.

INVERTER gate

A	Q
0	1
	0

INPUT



OUTPUT



6. What is the function of an INVERTER GATE?
